

FIG. 1 PRIOR ART

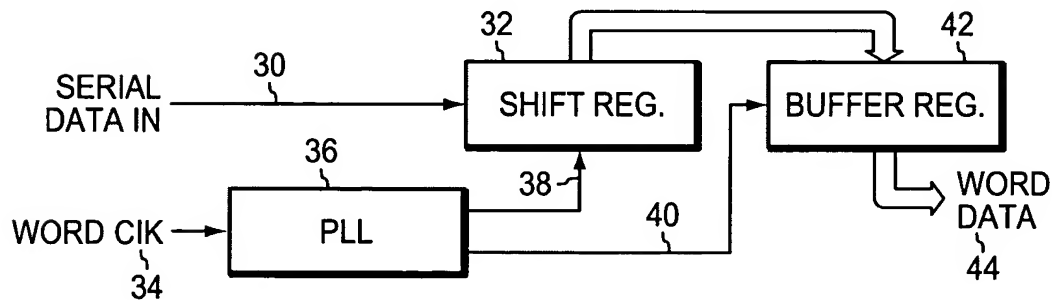


FIG. 2 PRIOR ART

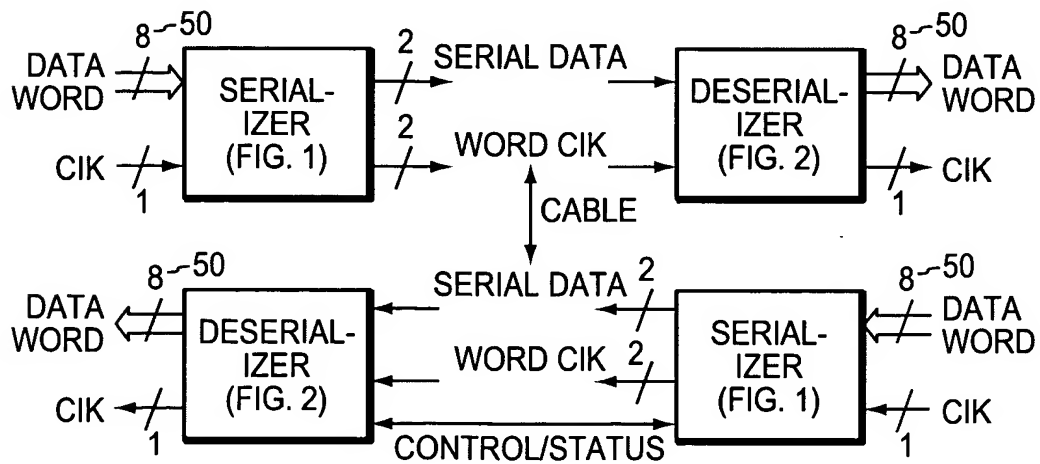


FIG. 3 PRIOR ART

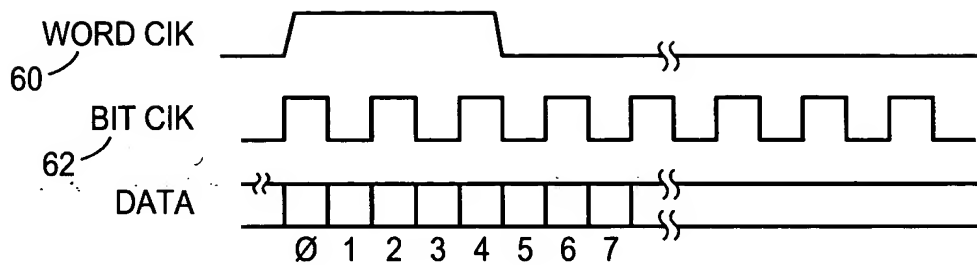


FIG. 4 PRIOR ART

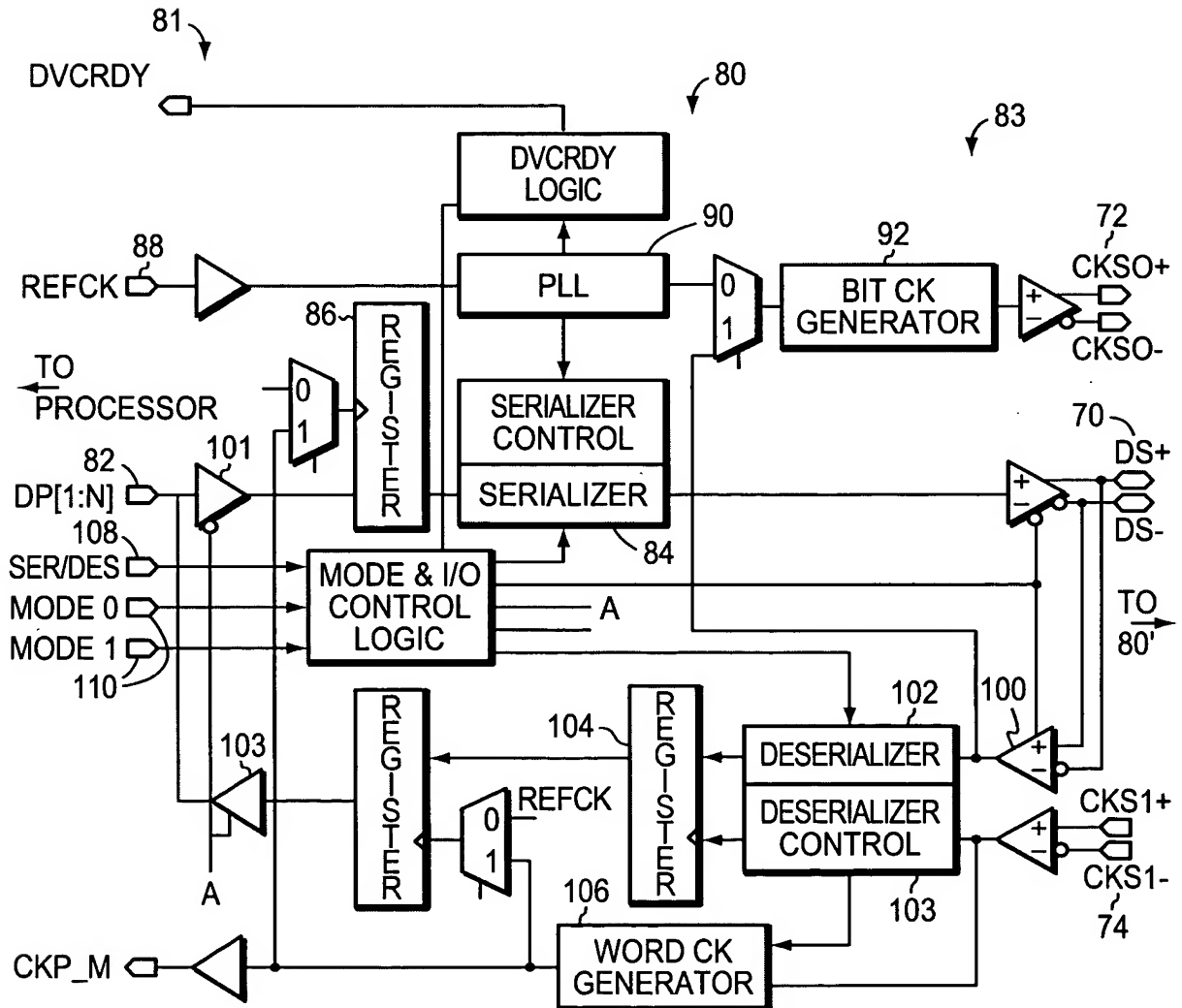
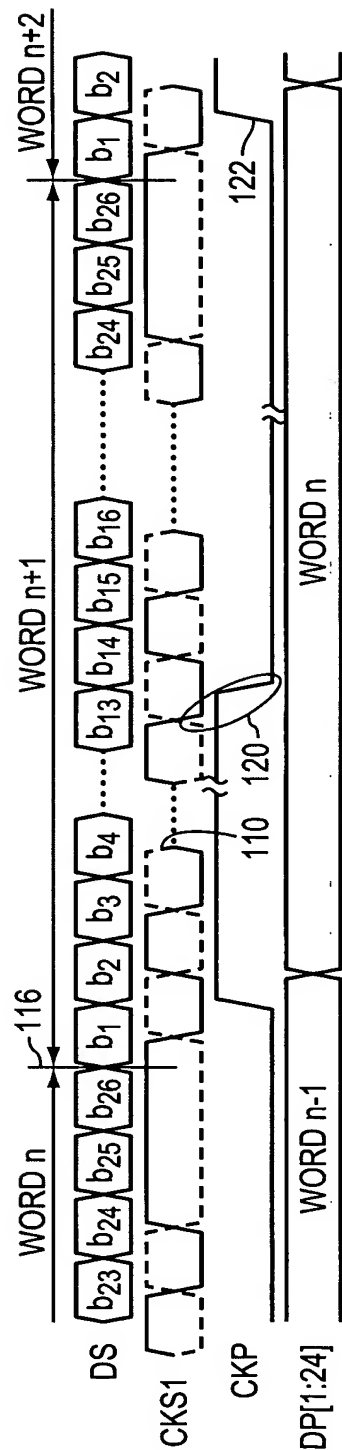
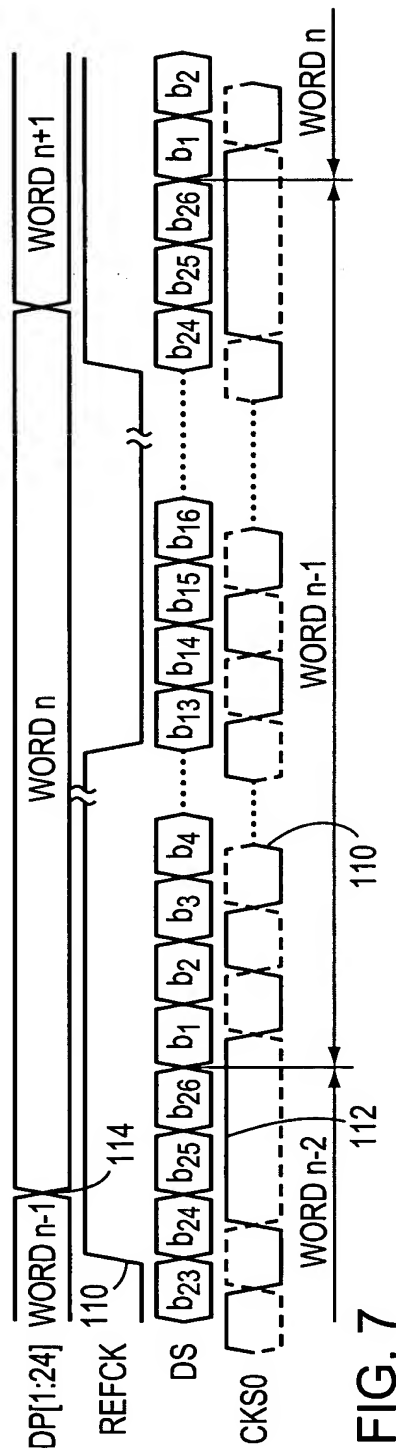


FIG. 5

MODE #	MODE0	MODE1	SER/DES	OPERATING MODE
0	0	0	X	POWER DOWN
1	0	1	0	DESERIALIZER PASSING MASTER CLOCK
1	0	1	1	SERIALIZER
2	1	0	0	DESERIALIZER
2	1	0	1	SERIALIZER WITH TURNAROUND CLOCK
3	1	1	0	DESERIALIZER
3	1	1	1	SERIALIZER

FIG. 6



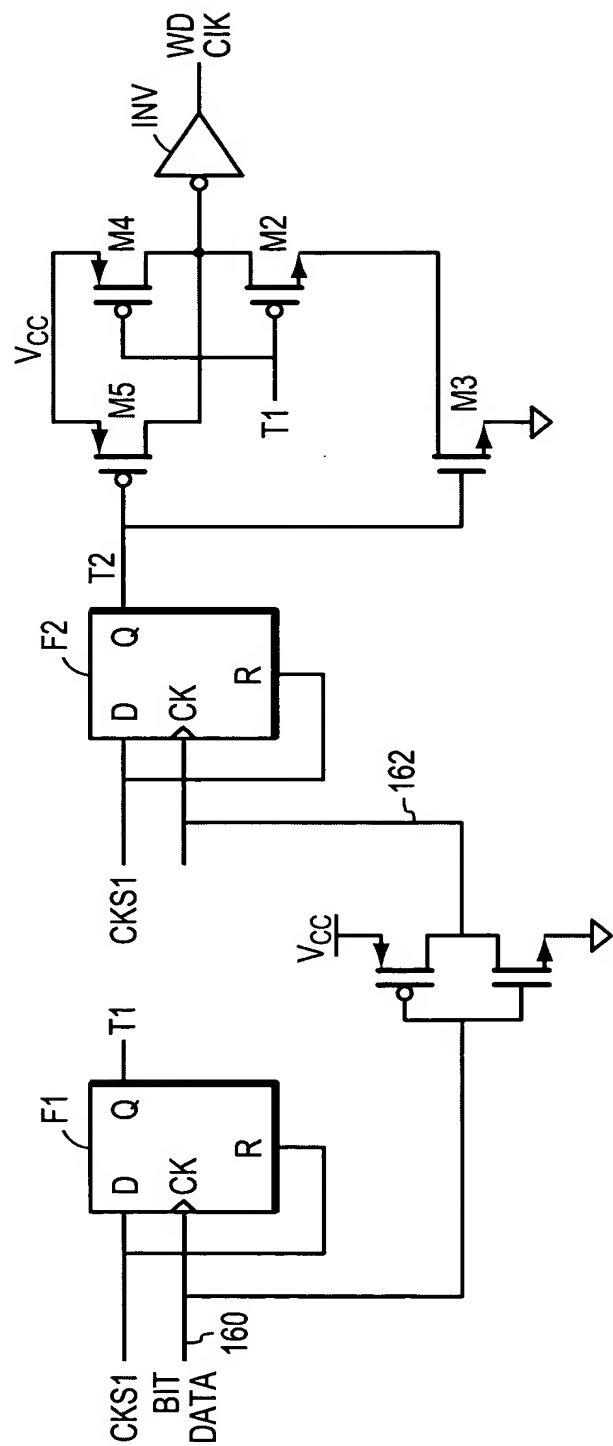


FIG. 8B

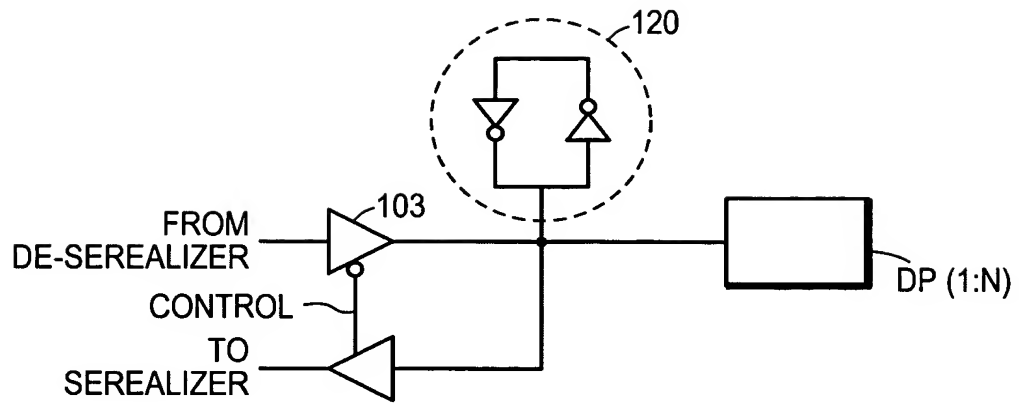


FIG. 9

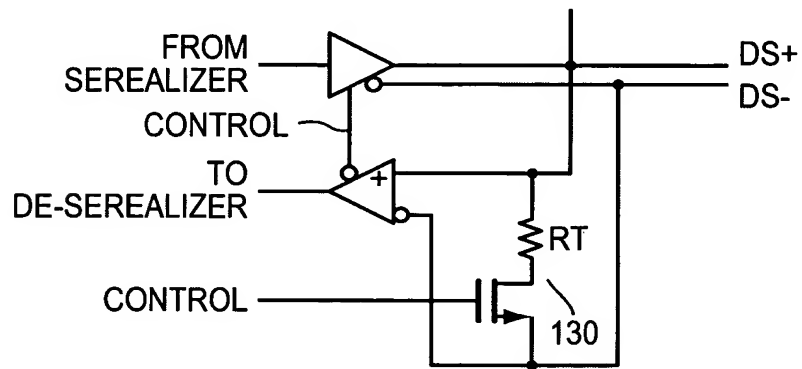


FIG. 10

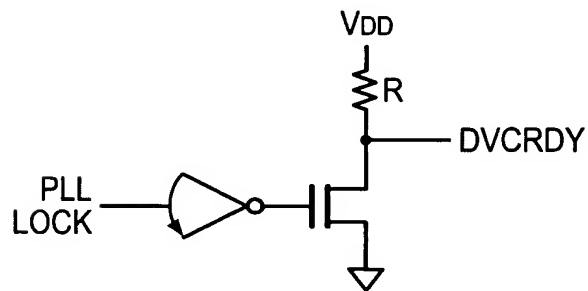


FIG. 11

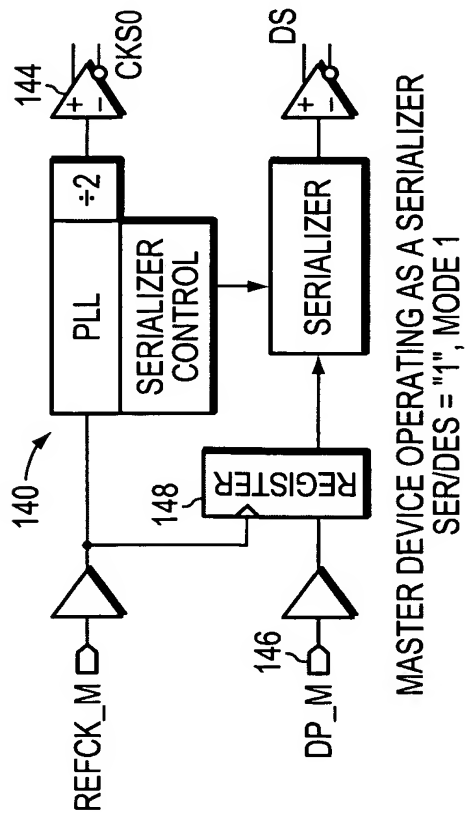


FIG. 12

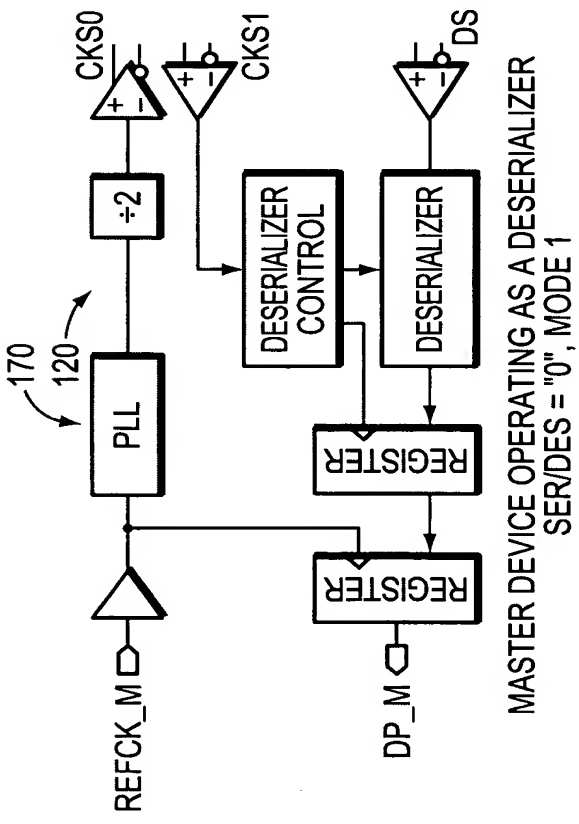
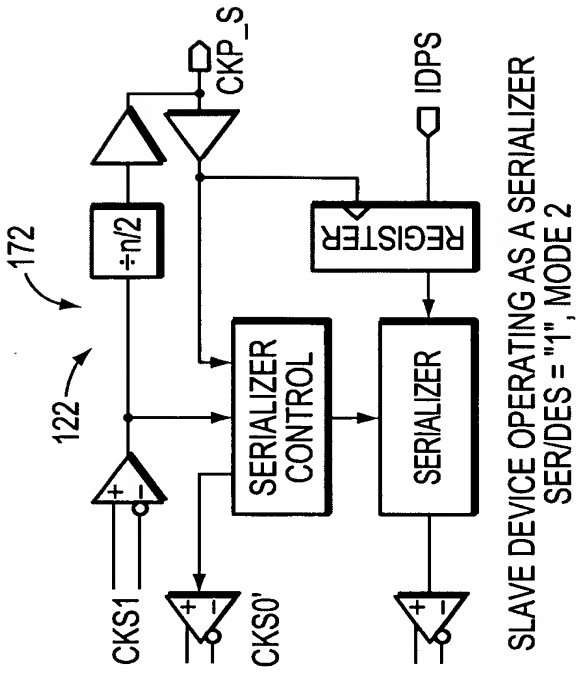
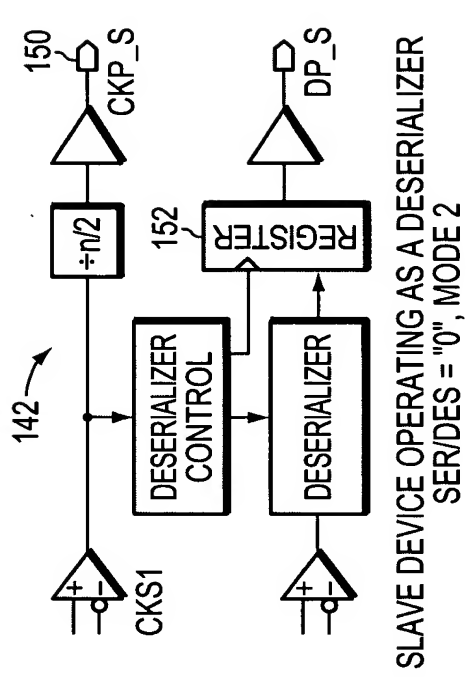
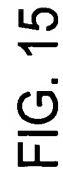
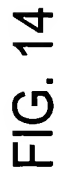


FIG. 13





+

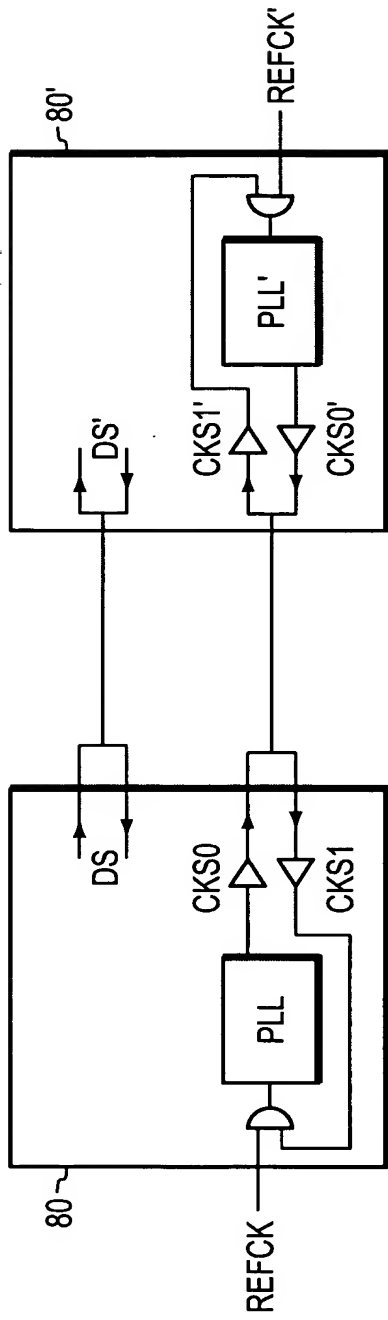


FIG. 16

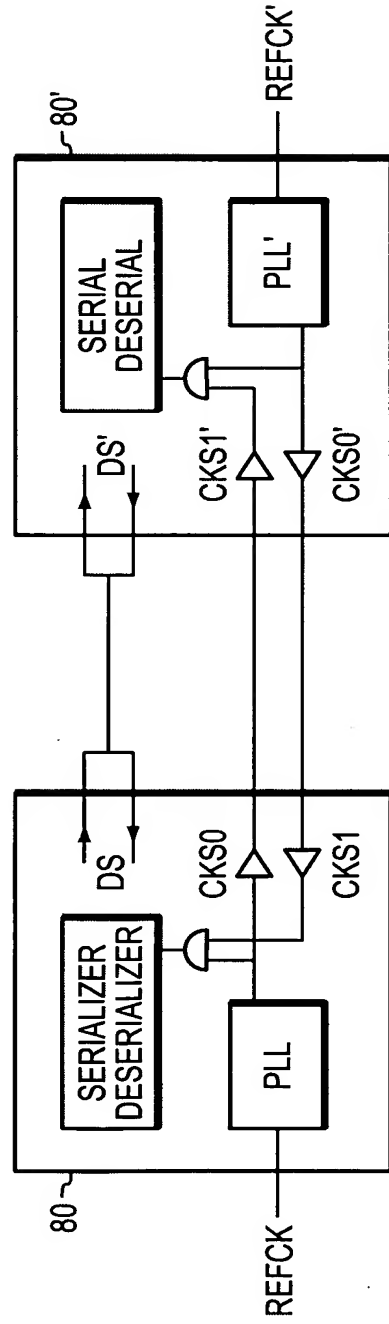


FIG. 17

+

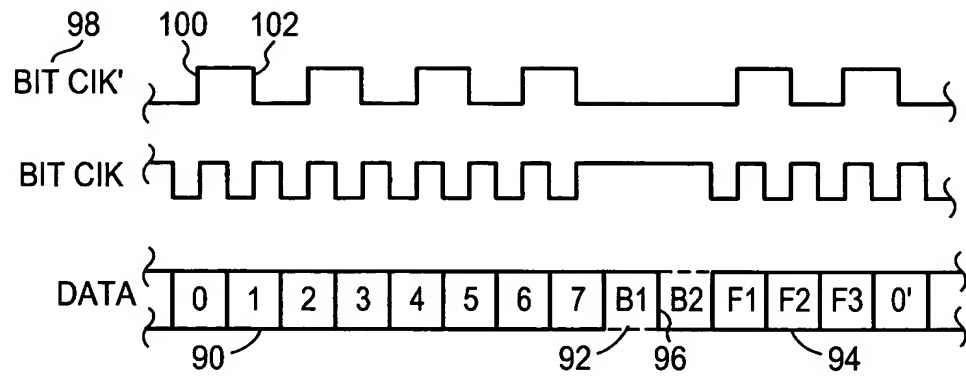


FIG. 18

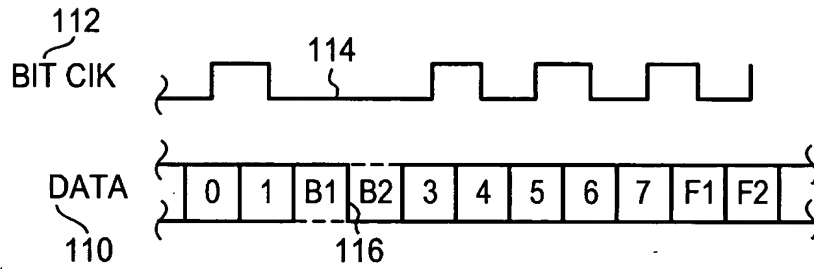


FIG. 19A

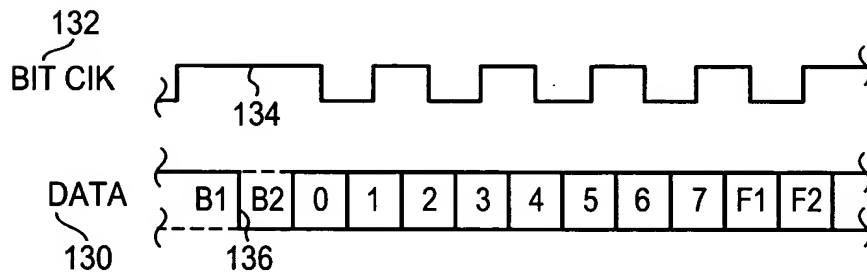


FIG. 19B

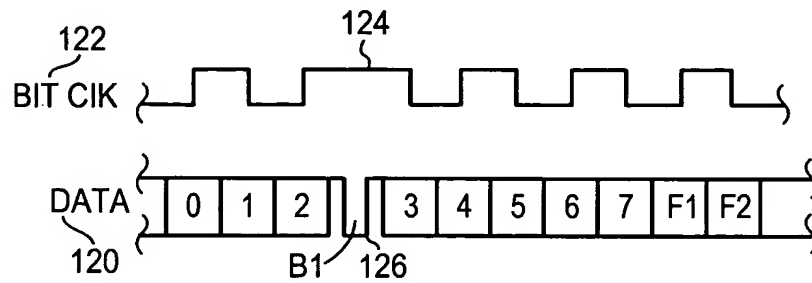


FIG. 20A

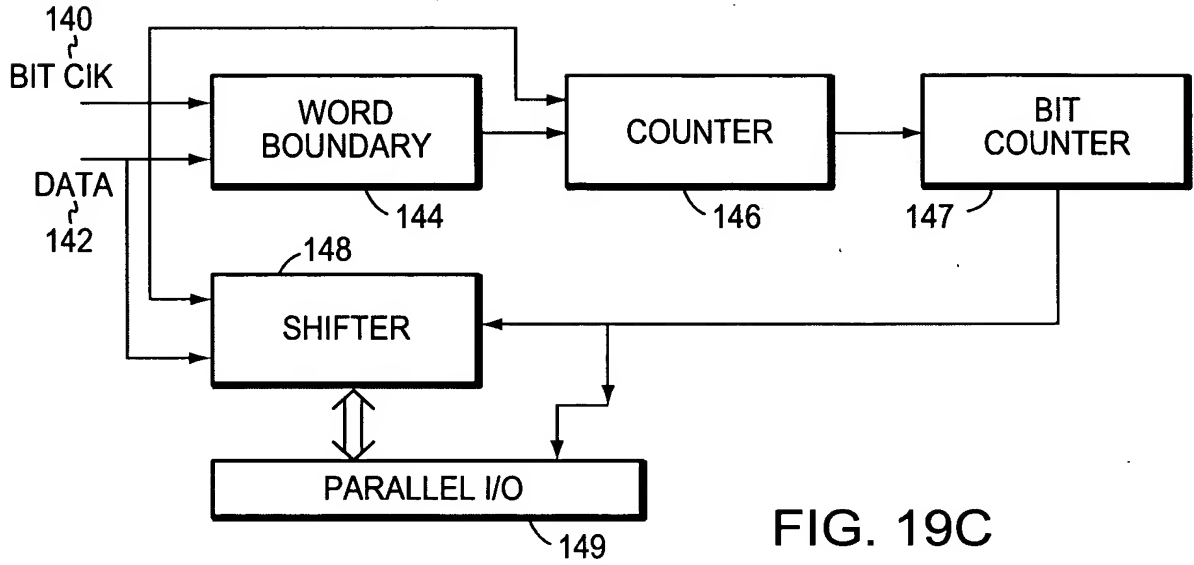


FIG. 19C

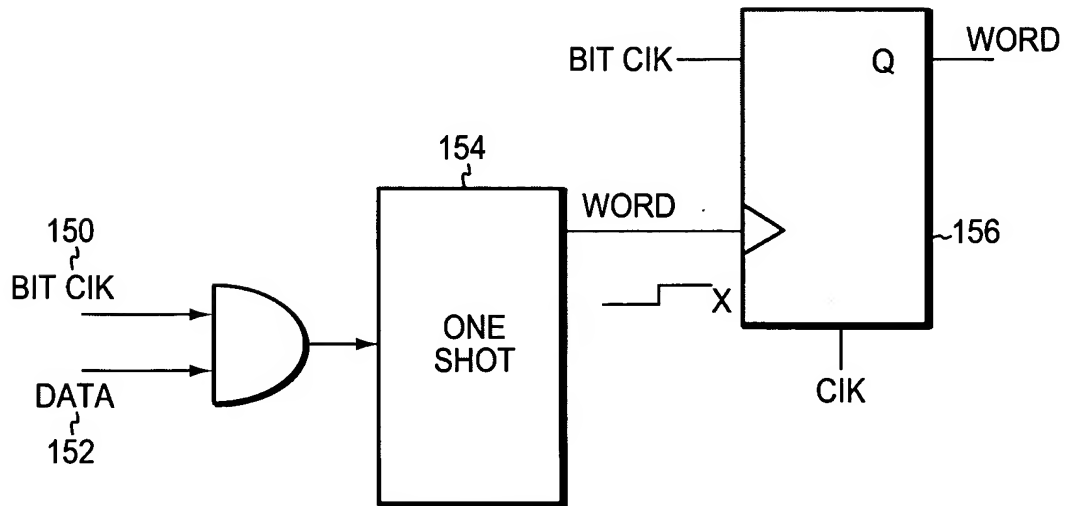


FIG. 20B